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Takuya. MAENISHI, Yasuhiro. YOSHIDA, Ikuo.
YAMASAKI, Akihito.

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(74) Agents: KAWAMIYA, Osamu et al.; AOYAMA & PART-
NERS, IMP Building, 3-7, Shiromi 1-chome, Chuo-ku, Os-
aka-shi, Osaka, 5400001 (JP).

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(71) Applicant (*for all designated States except US*): MAT-
SUSHITA ELECTRIC INDUSTRIAL CO., LTD.
[JP/JP]; 1006, Oaza Kadoma, Kadoma-shi, Osaka,
5718501 (JP).

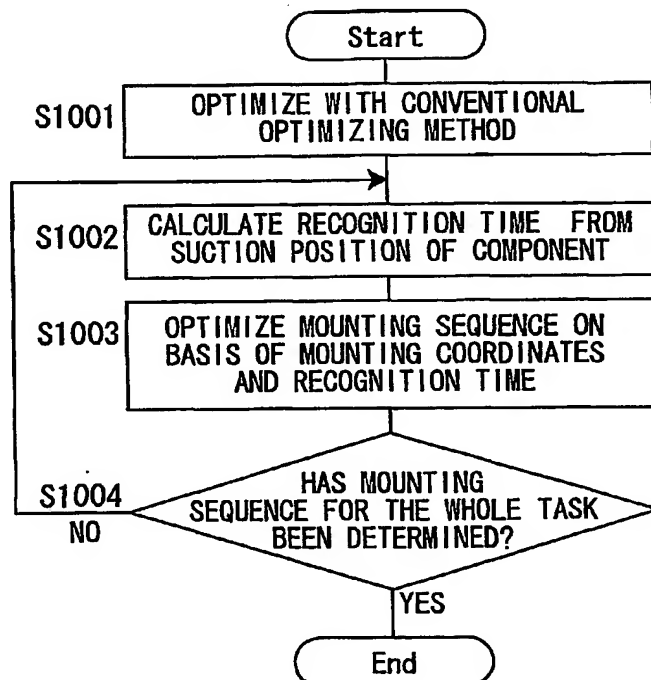
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(72) Inventors; and

(75) Inventors/Applicants (*for US only*): YAMAZAKI,

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(54) Title: COMPONENT MOUNTING SEQUENCE OPTIMIZING METHOD, COMPONENT MOUNTING DEVICE, PRO-
GRAM FOR EXECUTING COMPONENT MOUNTING SEQUENCE OPTIMIZING METHOD, AND RECORDING MEDIUM
IN WHICH THE PROGRAM IS RECORDED



(57) Abstract: The present invention provides a component mounting sequence optimizing method, a component mounting device, a program for executing the component mounting sequence optimizing method, and recording medium in which the program is recorded, in the component mounting device with a component holding head capable of holding a plurality of components and having a component recognition camera. The invention includes a component mounting sequence optimizing device (300) and a mounting sequence for components is determined on basis of a result of comparison between conveyance times for the components and recognition times required for recognitions of the components. Thus cycle time for mounting can be reduced, in comparison with conventional arts, in mounting of components with a component holding head having a plurality of component holding members and having a component recognition camera.

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

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A	WO 02/13590 A (MATSUSHITA ELECTRIC IND CO LTD) 14 February 2002 (2002-02-14) page 2, line 24 - page 3, line 4; figure 1 -----	1-12
A	WO 00/67542 A (SIEMENS AKTIENGESSELLSCHAFT) 9 November 2000 (2000-11-09) abstract -----	1,4,7,10

☐ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

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